

FERROELECTRIC MEMORY INPUT/OUTPUT APPARATUS**ABSTRACT**

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A memory system. The system includes at least two ferroelectric memory devices arranged sequentially. Each memory device has a data in signal and a data out signal, and the data out signal each memory device is transmitted as the data in signal of the next device in sequence. A system controller generates an initial data in signal for the first memory device.

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A data bus transfers data between each memory device and the system controller and an address bus provide addressing of the memory devices.